

Mymensingh Polytechnic Institute

Mymensingh

Electronics Technology

Semester Plan (3rd Semester)

T P C
2 3 3

Subject- Digital Electronics -I.(26831)

Week	Theory			Practical	
	Content (Specific Objective)	Activity		Content(Practical Job No)	Activity
		Class Test	Learning Materials		
1	Fundamentals of digital electronics. (1.1-1.4)		Ref. books, white board, marker, duster.	Verify the truth tables of logic gates..	
2	Number systems and codes.(2.1-2.8)			Do	
3	Logic gates.(3.1-3.5)			Observe the functions of adder & subtractor..	
4	Simplification of logic circuits.(4.1-4.5)	CT 1		Do	
5	Simplification of logic circuits.(4.6-4.9)			Verify the output operation of binary 4-bit parallel adder..	
6	Digital IC and Logic Family. (5.1-5.6)	QT 1		Verify the operation of binary comparator	
7	Combinational logic circuits.(6.1-6.5)			Do	
8	MID EXAM			MID EXAM	
9	Arithmetic logic circuits.(7.1-7.5)		Observe the operation of Encoder & Decoder		
10	Multiplexers and Demultiplexer.(8.1-8.5)	CT 2	Do		
11	Encoder and Decoder.(9.1-9.4)		Construct a 7 segment display driver.		
12	Encoder and Decoder. (9.4-9.7)		Do		
13	Sequential logic circuits.(10.1-10.5)	QT 2	Verify the truth table of different J-K flip-flops		
14	Sequential logic circuits. (10.6-10.10)		Observe the operation of Multiplexer & Demultiplexer.		
15	Review.		Do		

Teachers Name- Kazimohi Uddin

Part-time Teacher. Electronic.